

FIG. 1

FIG. 2 is a block diagram of a STA processing module 46, which is part of a station 22. The module 46 includes a controller 86, a modulator 82, a demodulator 84, a transmitter 52, a receiver 50, and an antenna switch 54. The controller 86 is connected to the modulator 82 and the demodulator 84. The modulator 82 is connected to the transmitter 52. The demodulator 84 is connected to the receiver 50. The transmitter 52 is connected to the antenna switch 54. The receiver 50 is connected to the antenna switch 54. The antenna switch 54 is connected to an antenna. The controller 86 also controls the gain control 92 and the RSSI 94. The gain control 92 is connected to the transmitter 52 and the receiver 50. The RSSI 94 is connected to the receiver 50. The channel state 96 is connected to the controller 86 and the demodulator 84. The TX frame data 88 is connected to the modulator 82. The RX frame data 90 is connected to the demodulator 84. The baseband signal 112 is connected to the receiver 50.

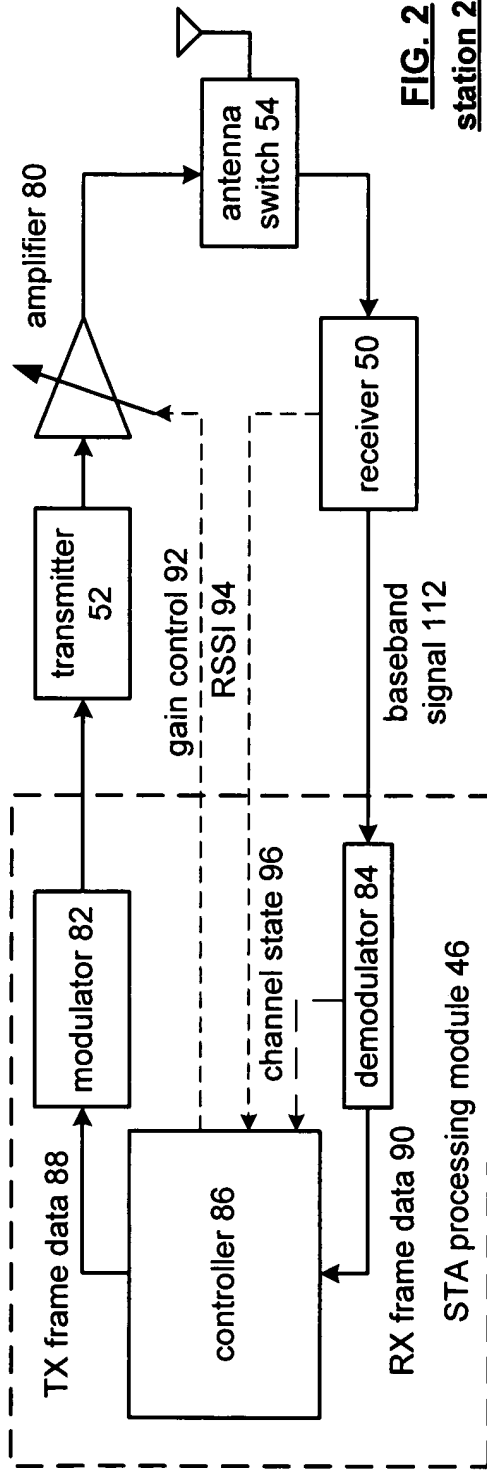


FIG. 2
station 22

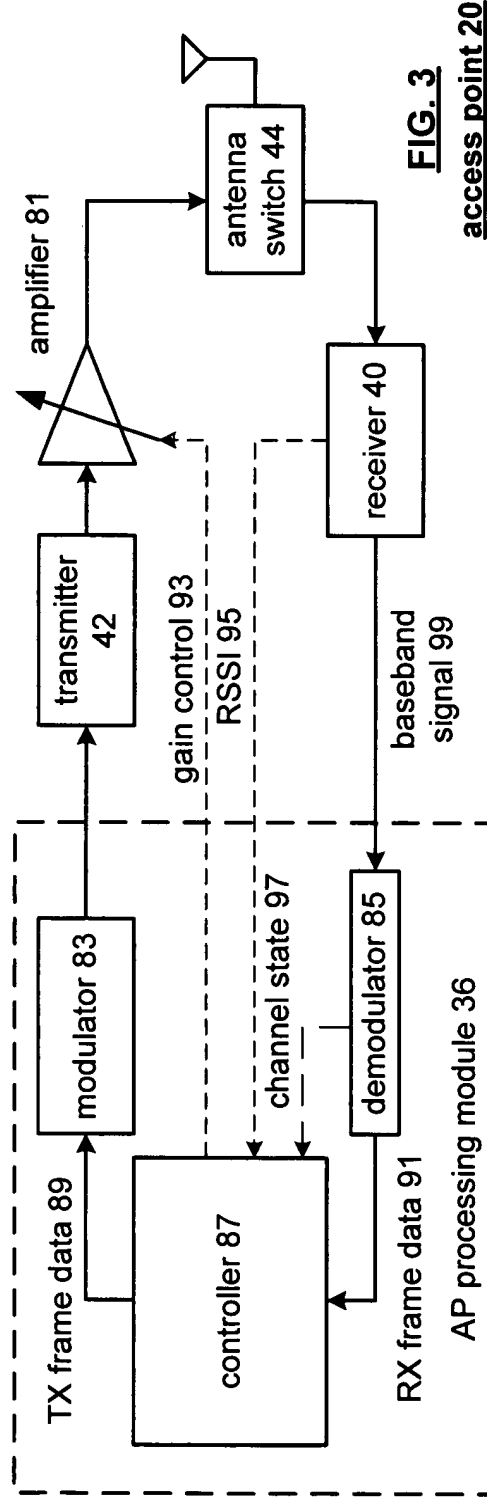


FIG. 3
access point 20

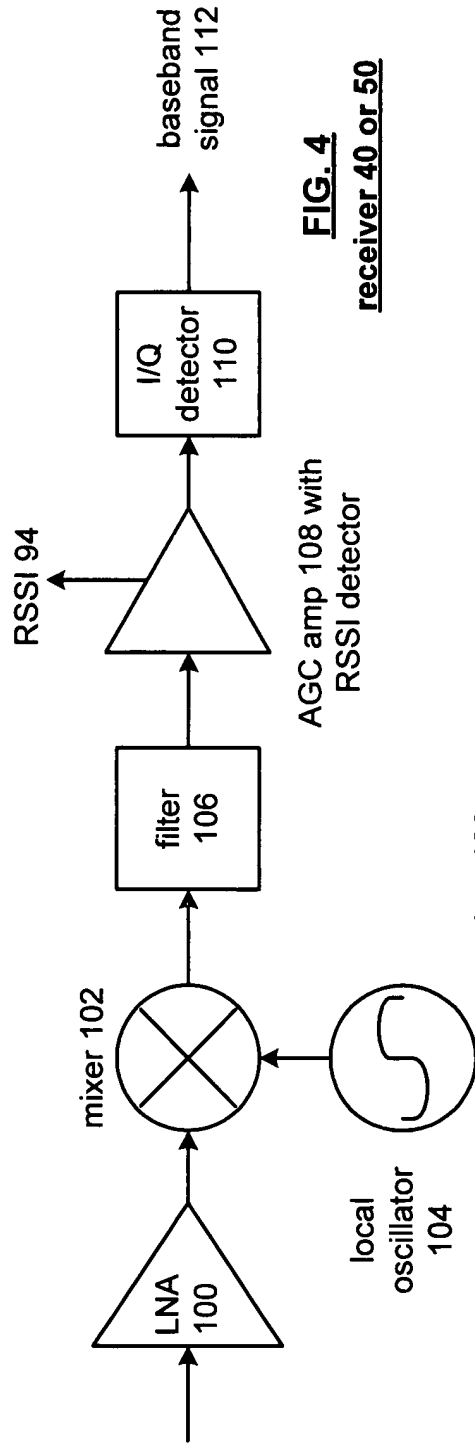


FIG. 4
receiver 40 or 50

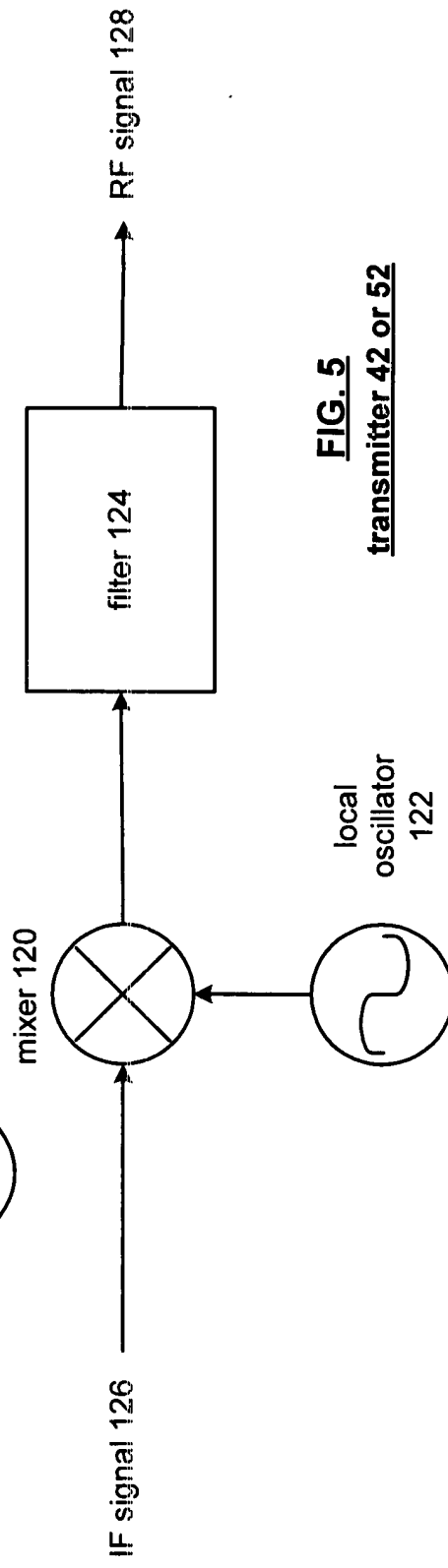


FIG. 5
transmitter 42 or 52

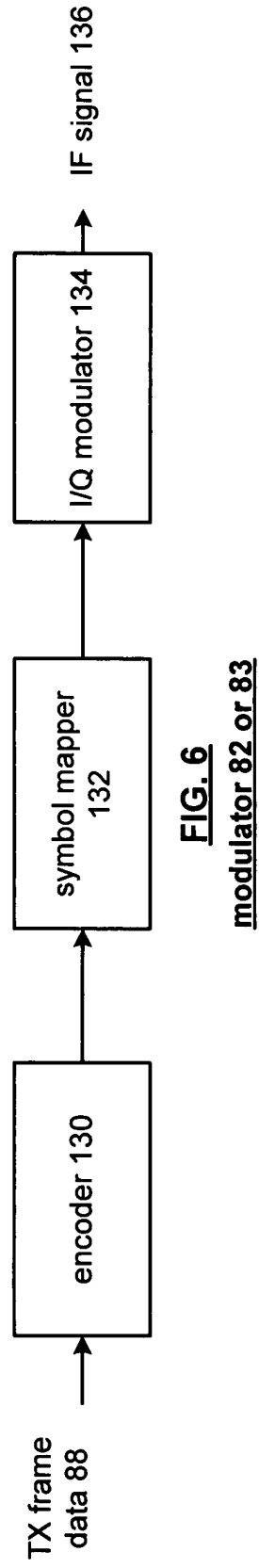


FIG. 6
modulator 82 or 83

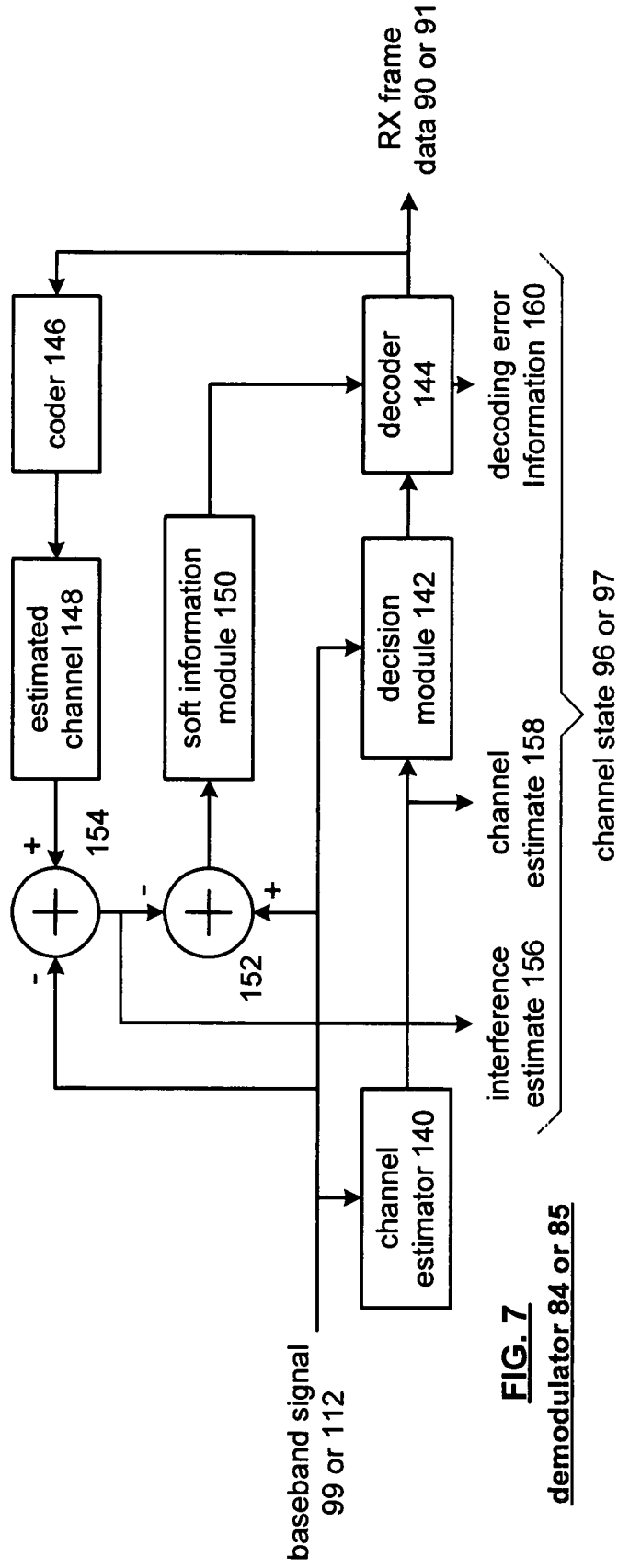


FIG. 7
demodulator 84 or 85

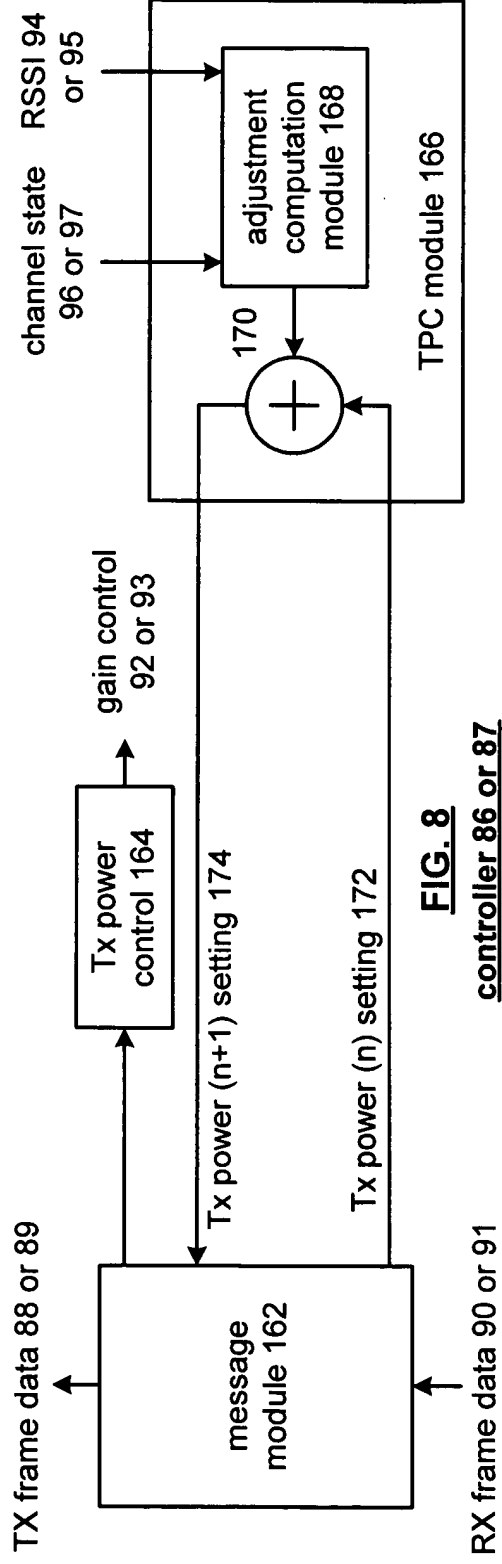


FIG. 8
controller 86 or 87

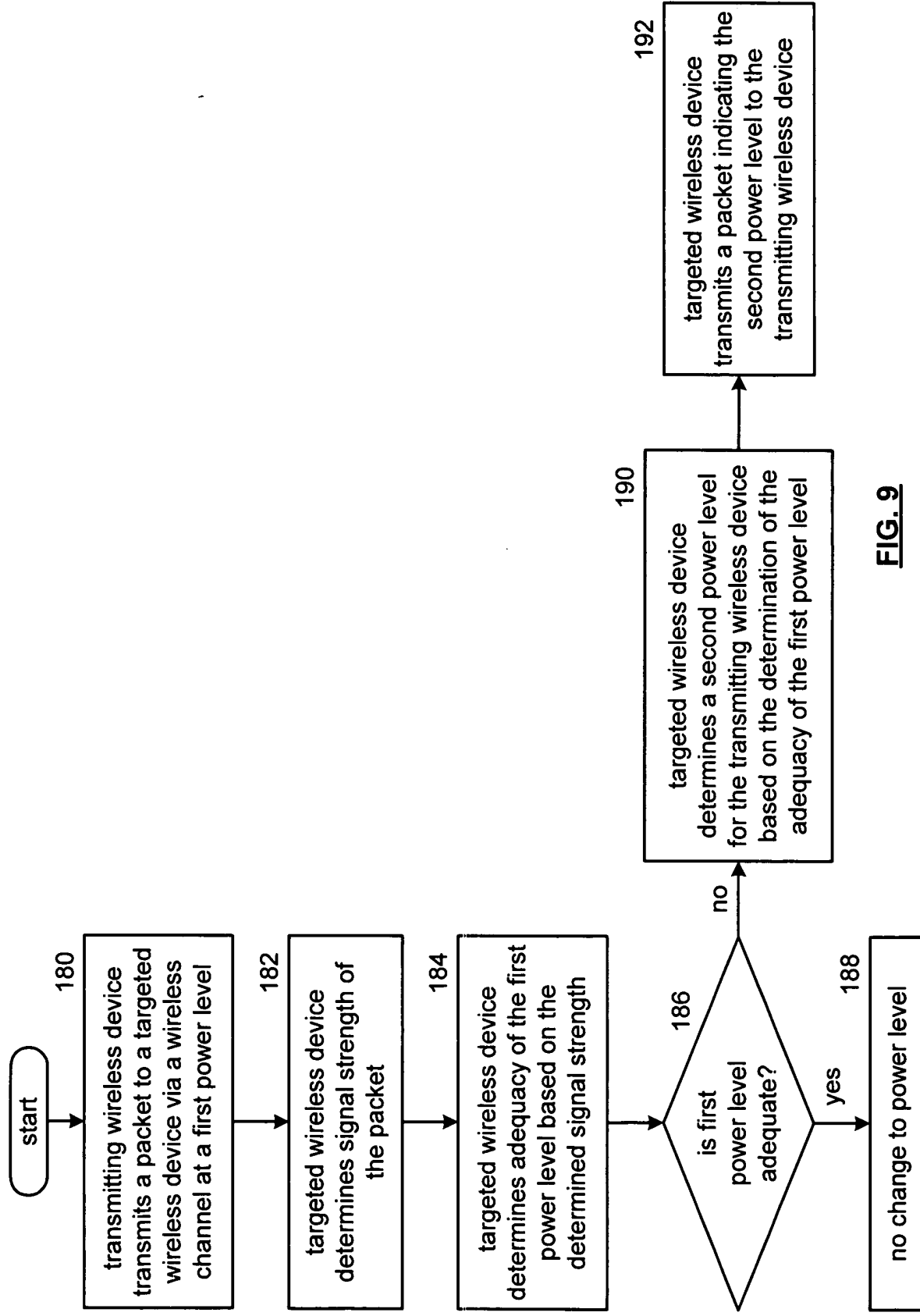


FIG. 9

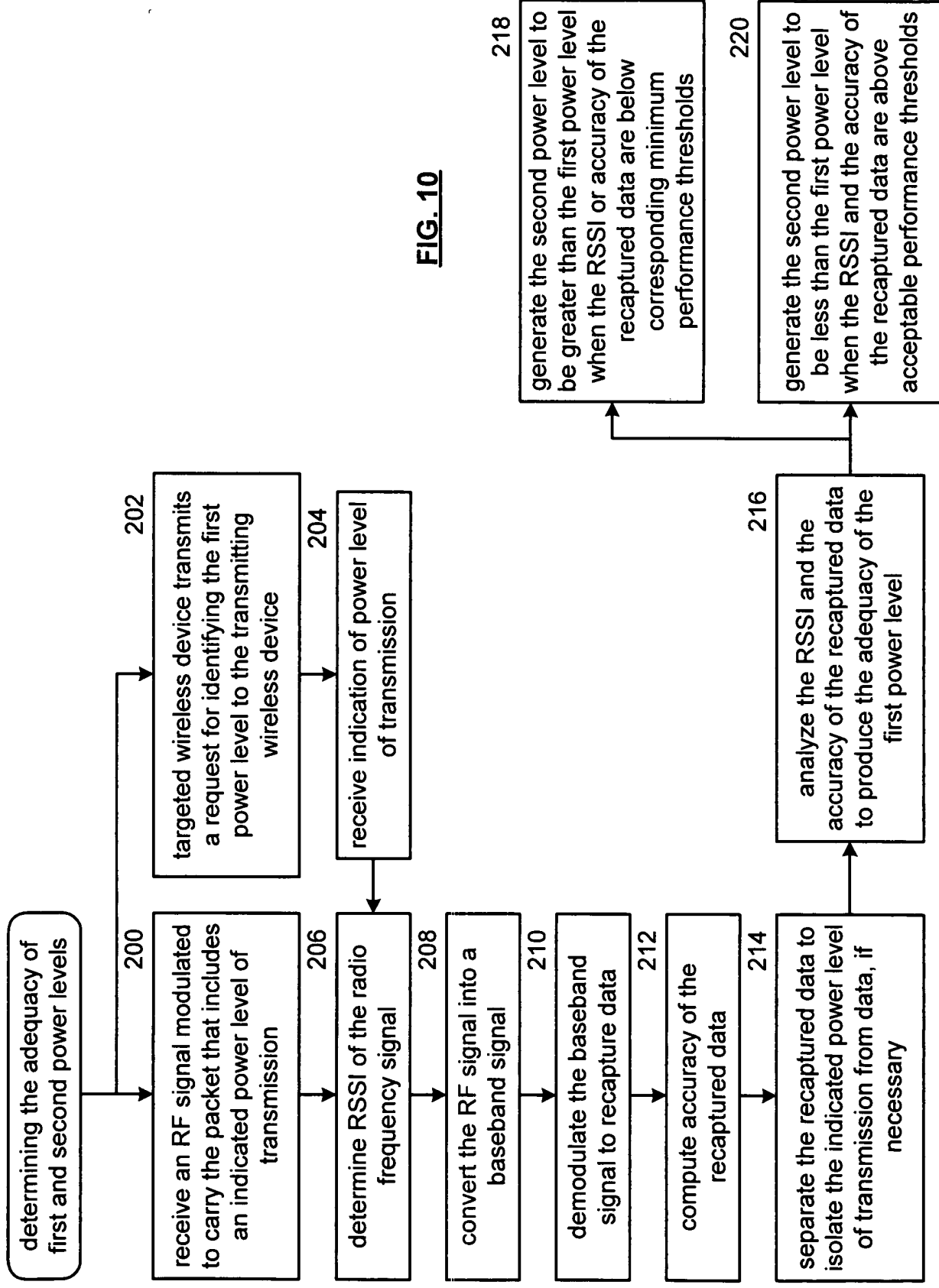


FIG. 10

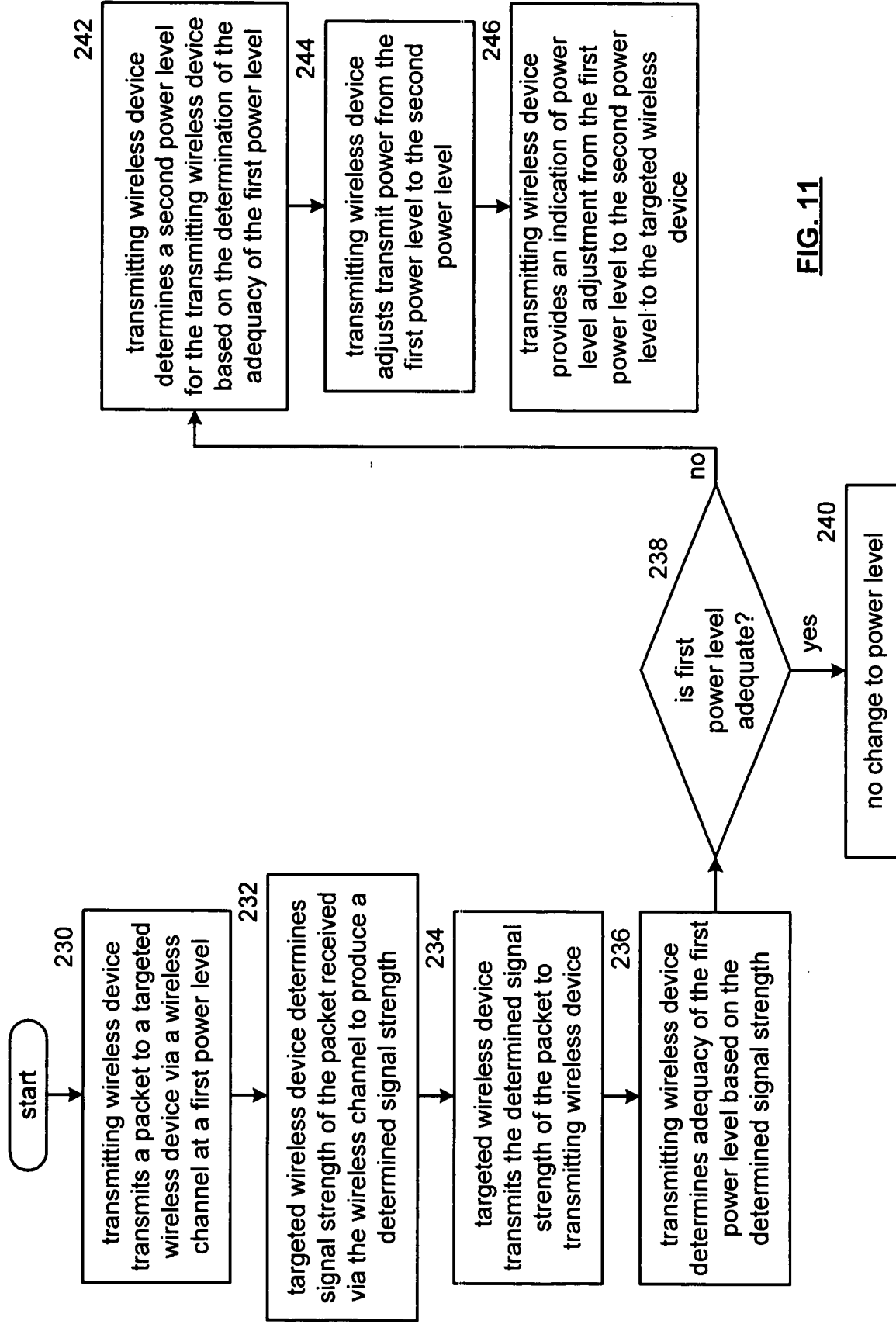


FIG. 11

